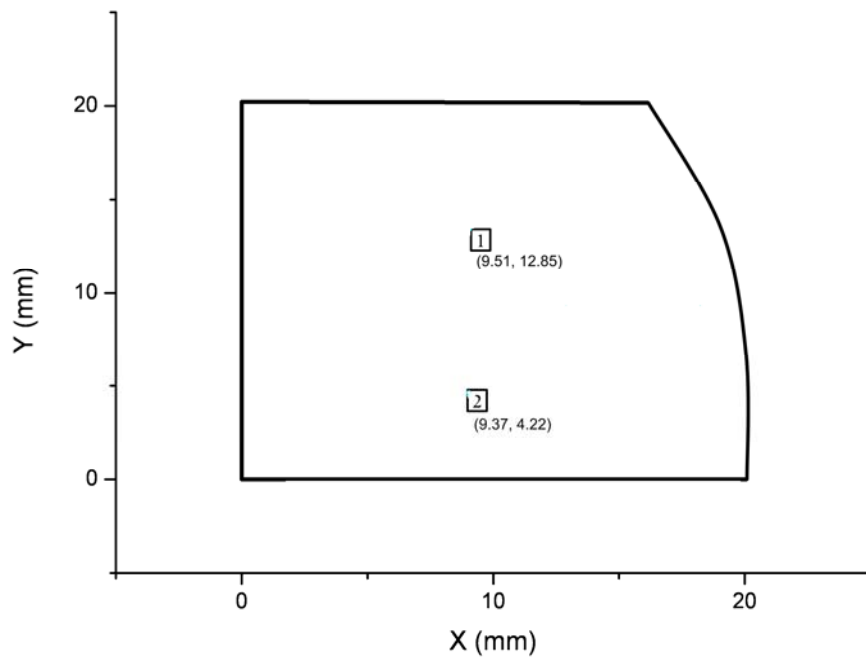


LRW-0037 Datasheet

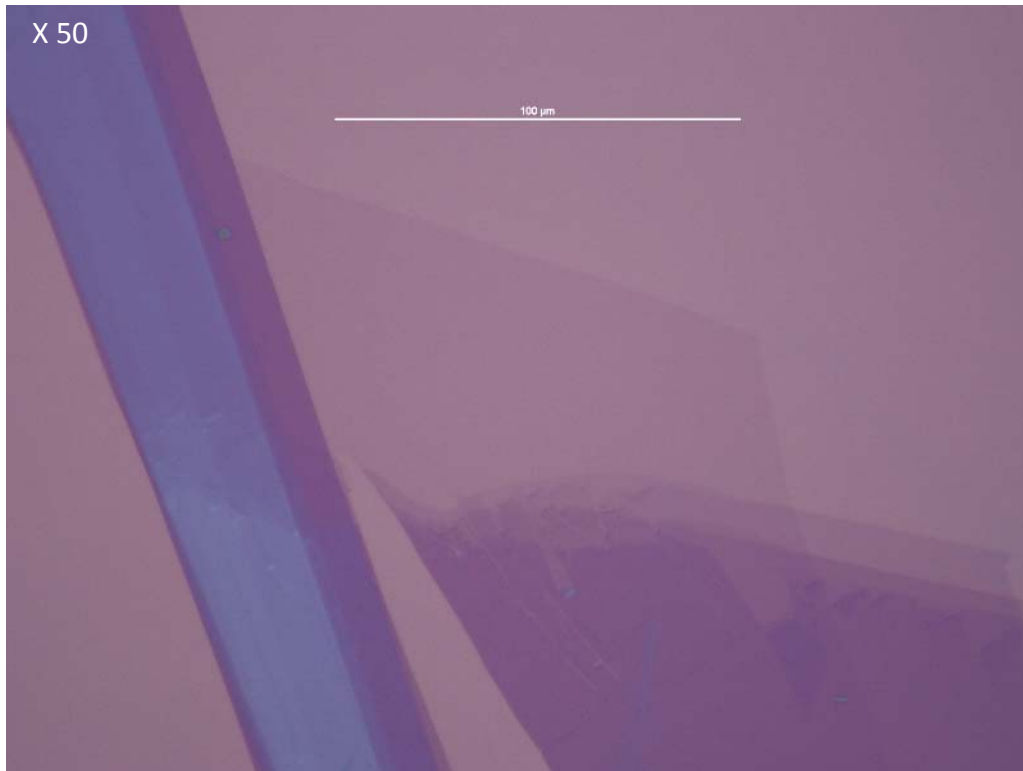
Parent Wafer

Si Orientation	<100>
Si Thickness	500-560 μ m
Resistivity	0.001-0.005 Ω cm
Oxide	300nm Dry Thermal Oxide

Substrate Map



Flake 1: x=9.51mm, y=12.85mm Area: >6700 μm^2 (single layer)



Flake 2: x=9.37mm, y=4.22mm Area: >32000 μm^2 (single layer)

