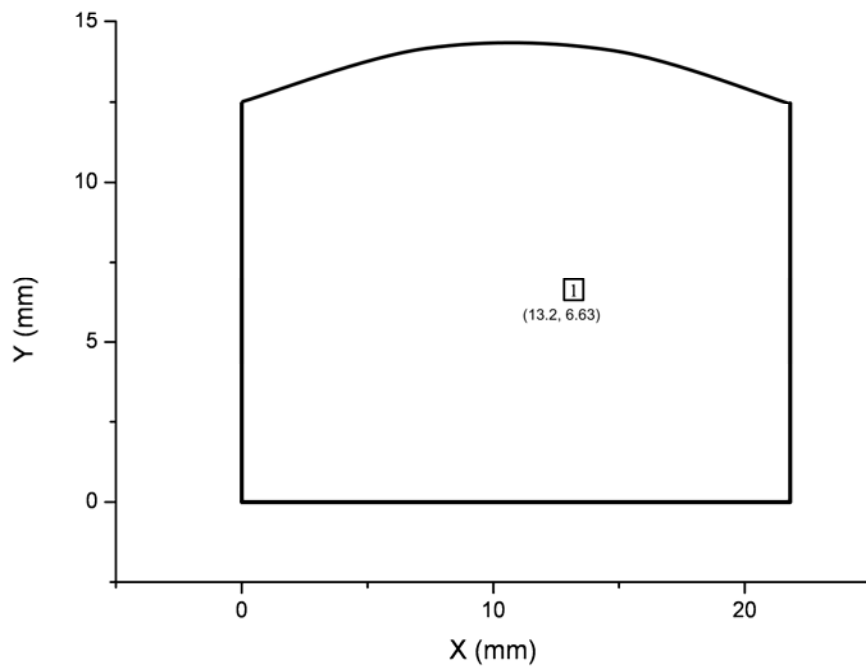


LRW-0058 Datasheet

Parent Wafer

Si Orientation	<100>
Si Thickness	500-560 μ m
Resistivity	0.001-0.005 Ω cm
Oxide	300nm Dry Thermal Oxide

Substrate Map



Flake 1: x=13.20mm, y=6.63mm Area: >10000 μm^2 (single layer)

