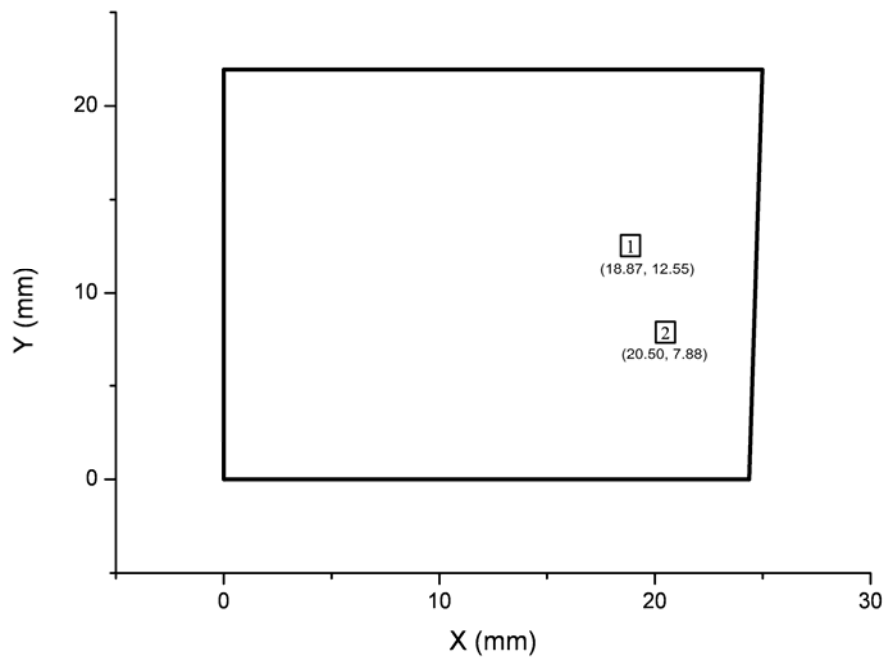


LRW-0061 Datasheet

Parent Wafer

Si Orientation	<100>
Si Thickness	500-560 μ m
Resistivity	0.001-0.005 Ω cm
Oxide	300nm Dry Thermal Oxide

Substrate Map



Flake 1: x=18.87mm, y=12.55mm Area: >177000 μm^2 (single layer)



Flake 2: x=20.50mm, y=7.88mm Area: >135000 μm^2 (single layer)

