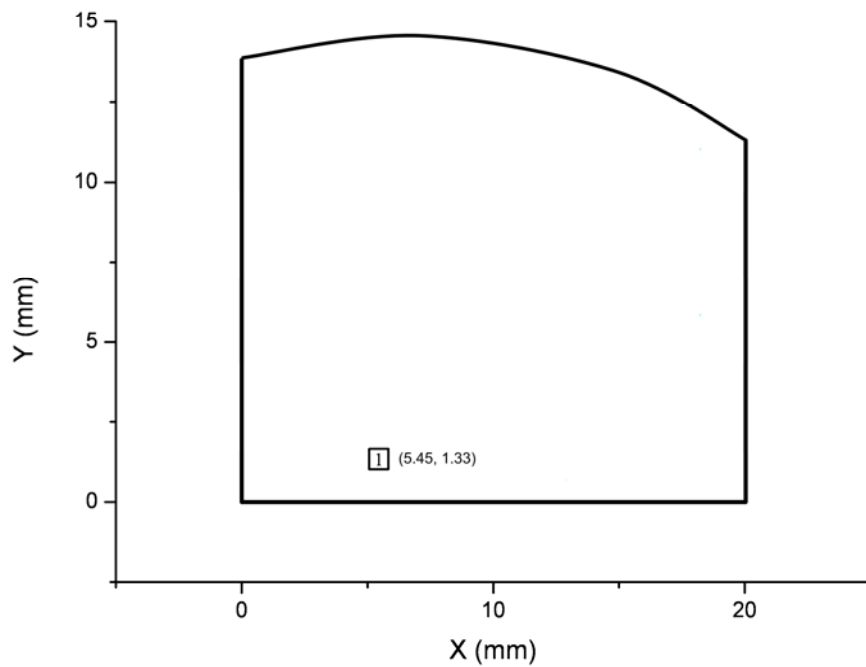


LRW-0069 Datasheet

Parent Wafer

Si Orientation	<100>
Si Thickness	500-560 μ m
Resistivity	0.001-0.005 Ω cm
Oxide	300nm Dry Thermal Oxide

Substrate Map



Flake 1: x=5.45mm, y=1.33mm Area: >89000 μm^2 (single layer)

