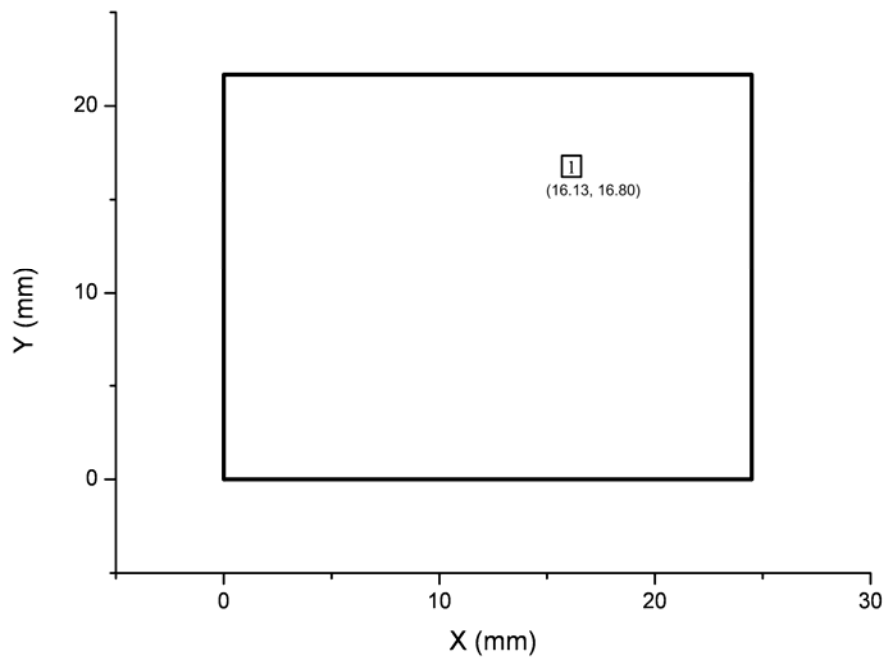


LRW-0077 Datasheet

Parent Wafer

Si Orientation	<100>
Si Thickness	500-560 μ m
Resistivity	0.001-0.005 Ω cm
Oxide	300nm Dry Thermal Oxide

Substrate Map



Flake 1: x=16.13mm, y=16.80mm Area: >13000 μm^2 (single layer)

